FIG. 1

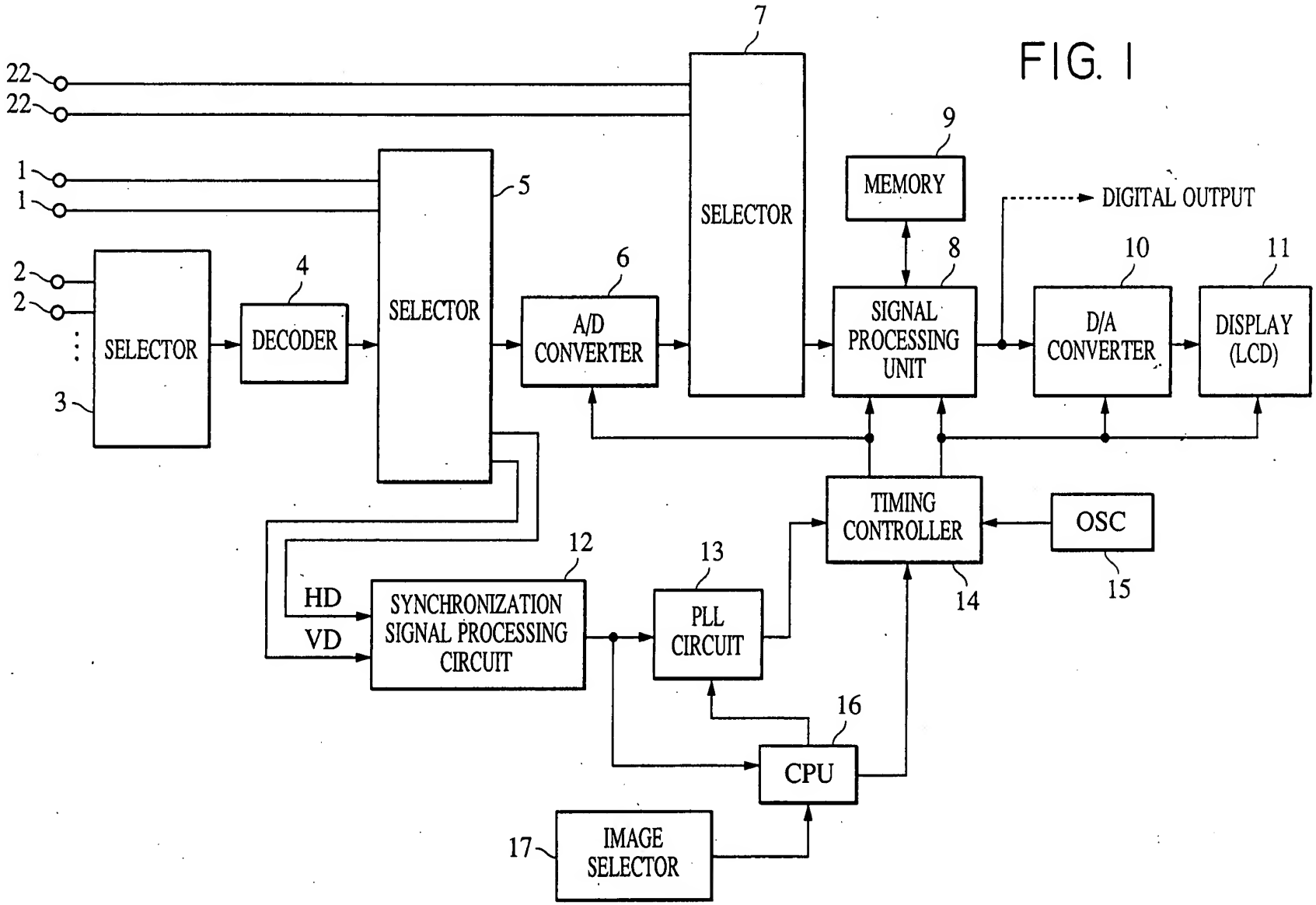

22
22
1
1
2
2
3
SELECTOR
DECODER
4
SELECTOR
5
SELECTOR
7
A/D CONVERTER
6
MEMORY
9
SIGNAL PROCESSING UNIT
8
DIGITAL OUTPUT
D/A CONVERTER
10
DISPLAY (LCD)
11
TIMING CONTROLLER
14
OSC
15
HD
VD
SYNCHRONIZATION SIGNAL PROCESSING CIRCUIT
12
PLL CIRCUIT
13
CPU
16
IMAGE SELECTOR
17

FIG. 2

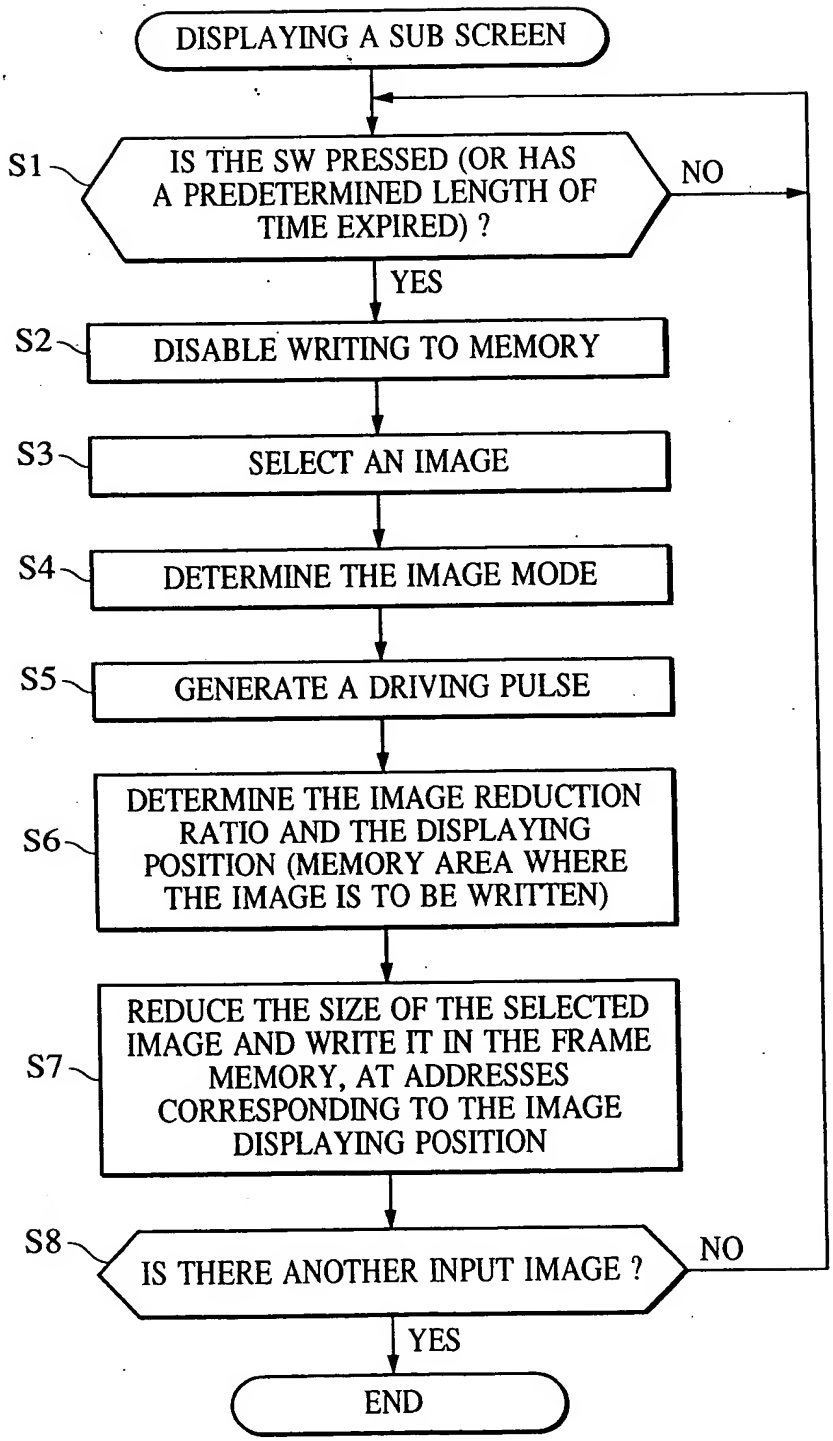

DISPLAYING A SUB SCREEN
S1
IS THE SW PRESSED (OR HAS A PREDETERMINED LENGTH OF TIME EXPIRED) ?
NO
YES
S2
DISABLE WRITING TO MEMORY
S3
SELECT AN IMAGE
S4
DETERMINE THE IMAGE MODE
S5
GENERATE A DRIVING PULSE
S6
DETERMINE THE IMAGE REDUCTION RATIO AND THE DISPLAYING POSITION (MEMORY AREA WHERE THE IMAGE IS TO BE WRITTEN)
S7
REDUCE THE SIZE OF THE SELECTED IMAGE AND WRITE IT IN THE FRAME MEMORY, AT ADDRESSES CORRESPONDING TO THE IMAGE DISPLAYING POSITION
S8
IS THERE ANOTHER INPUT IMAGE ?
NO
YES
END

FIG. 3A

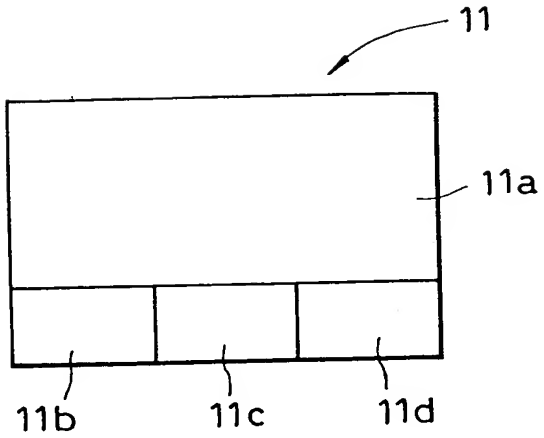


FIG. 3B

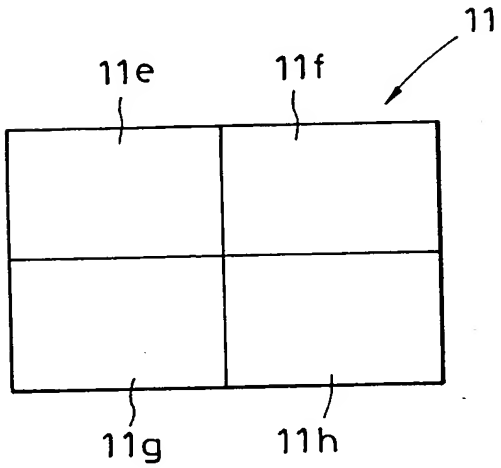


FIG. 3C

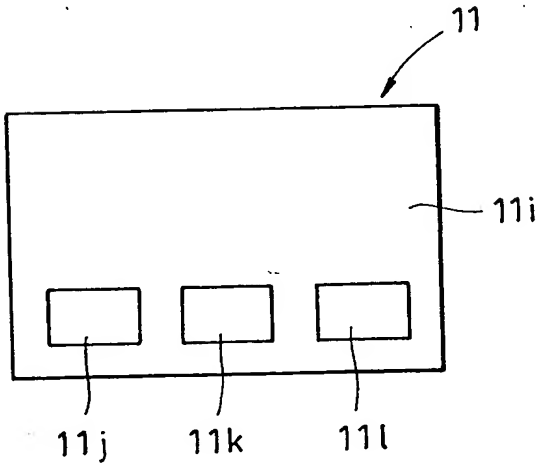


FIG. 4

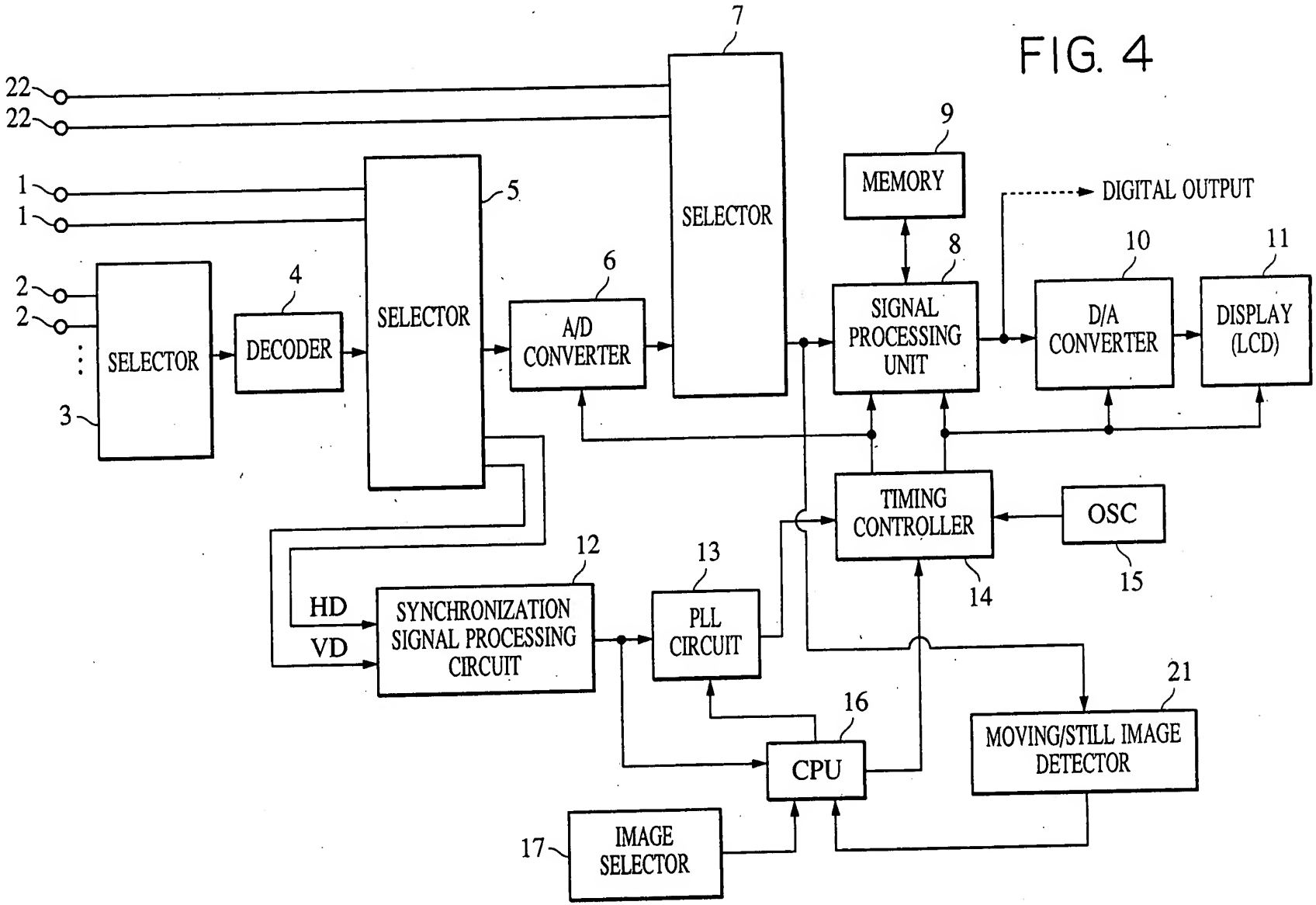

7
22
22
SELECTOR
9
MEMORY
DIGITAL OUTPUT
1
1
5
8
10
11
2
2
4
6
SELECTOR
DECODER
SELECTOR
A/D CONVERTER
SIGNAL PROCESSING UNIT
D/A CONVERTER
DISPLAY (LCD)
3
TIMING CONTROLLER
OSC
12
13
HD
VD
SYNCHRONIZATION SIGNAL PROCESSING CIRCUIT
PLL CIRCUIT
14
15
21
16
CPU
MOVING/STILL IMAGE DETECTOR
17
IMAGE SELECTOR

FIG. 5

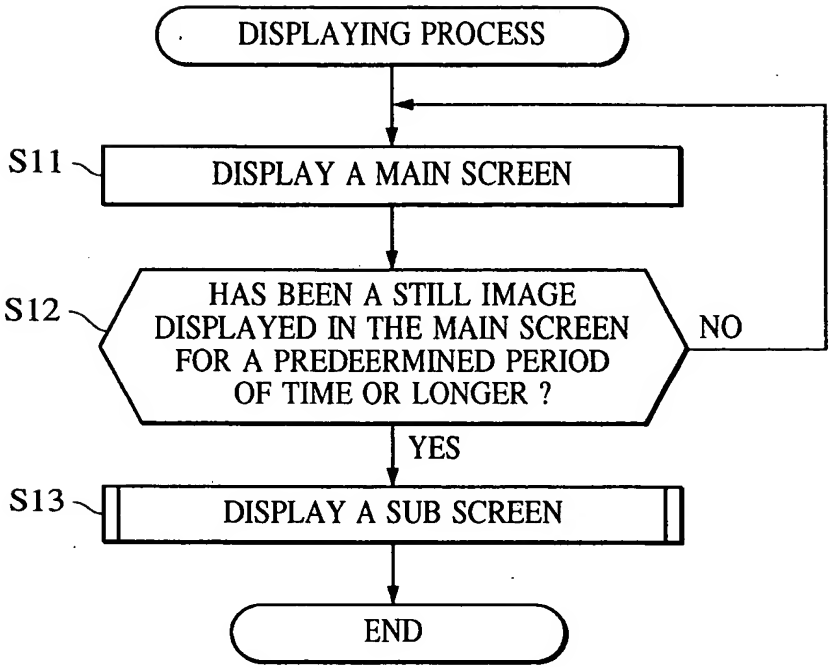

DISPLAYING PROCESS
S11
DISPLAY A MAIN SCREEN
S12
HAS BEEN A STILL IMAGE DISPLAYED IN THE MAIN SCREEN FOR A PREDEERMINED PERIOD OF TIME OR LONGER ?
NO
YES
S13
DISPLAY A SUB SCREEN
END

FIG. 6

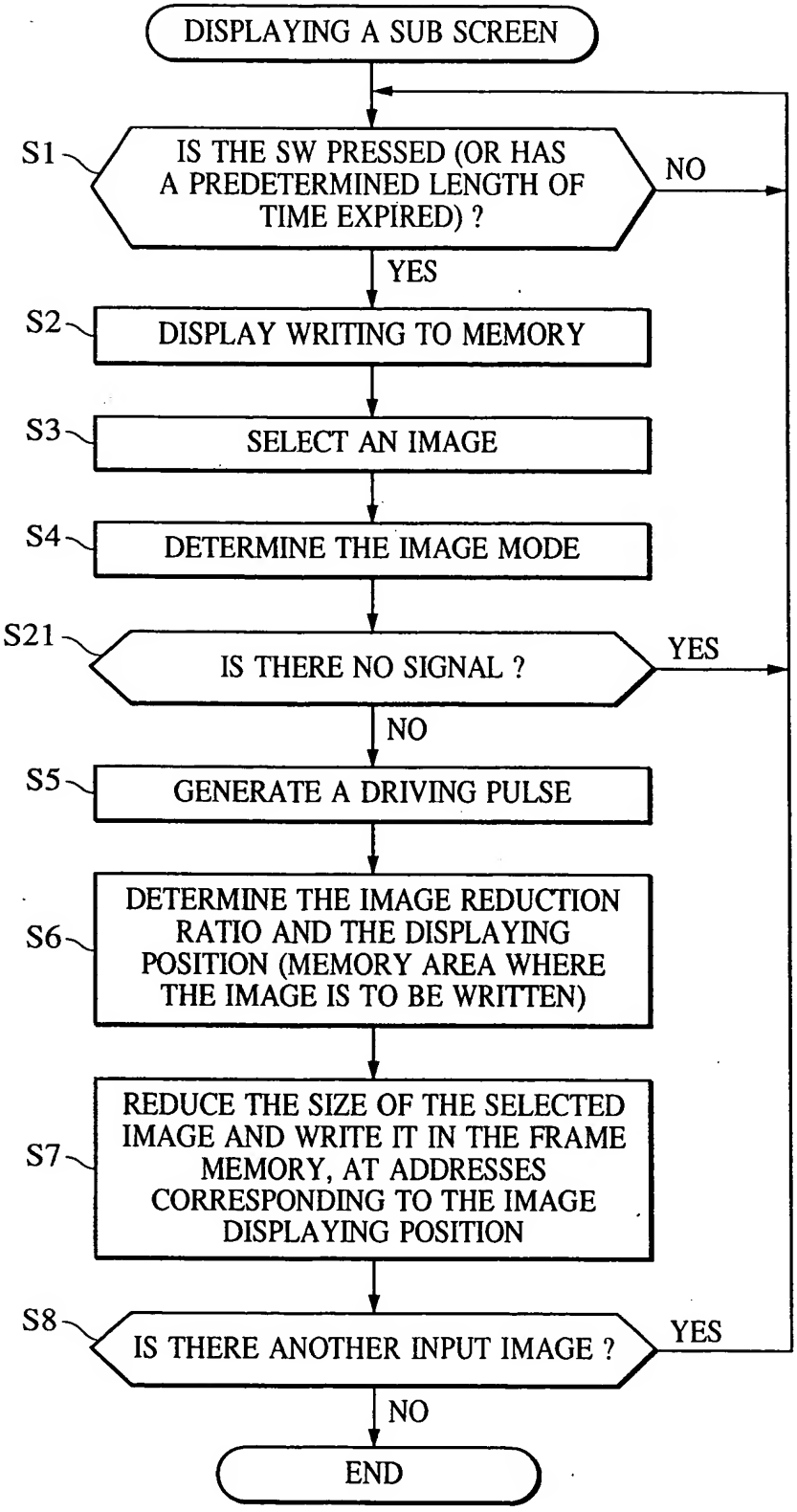

DISPLAYING A SUB SCREEN
S1
IS THE SW PRESSED (OR HAS A PREDETERMINED LENGTH OF TIME EXPIRED) ?
NO
YES
S2
DISPLAY WRITING TO MEMORY
S3
SELECT AN IMAGE
S4
DETERMINE THE IMAGE MODE
S21
IS THERE NO SIGNAL ?
YES
NO
S5
GENERATE A DRIVING PULSE
S6
DETERMINE THE IMAGE REDUCTION RATIO AND THE DISPLAYING POSITION (MEMORY AREA WHERE THE IMAGE IS TO BE WRITTEN)
S7
REDUCE THE SIZE OF THE SELECTED IMAGE AND WRITE IT IN THE FRAME MEMORY, AT ADDRESSES CORRESPONDING TO THE IMAGE DISPLAYING POSITION
S8
IS THERE ANOTHER INPUT IMAGE ?
YES
NO
END

FIG. 7

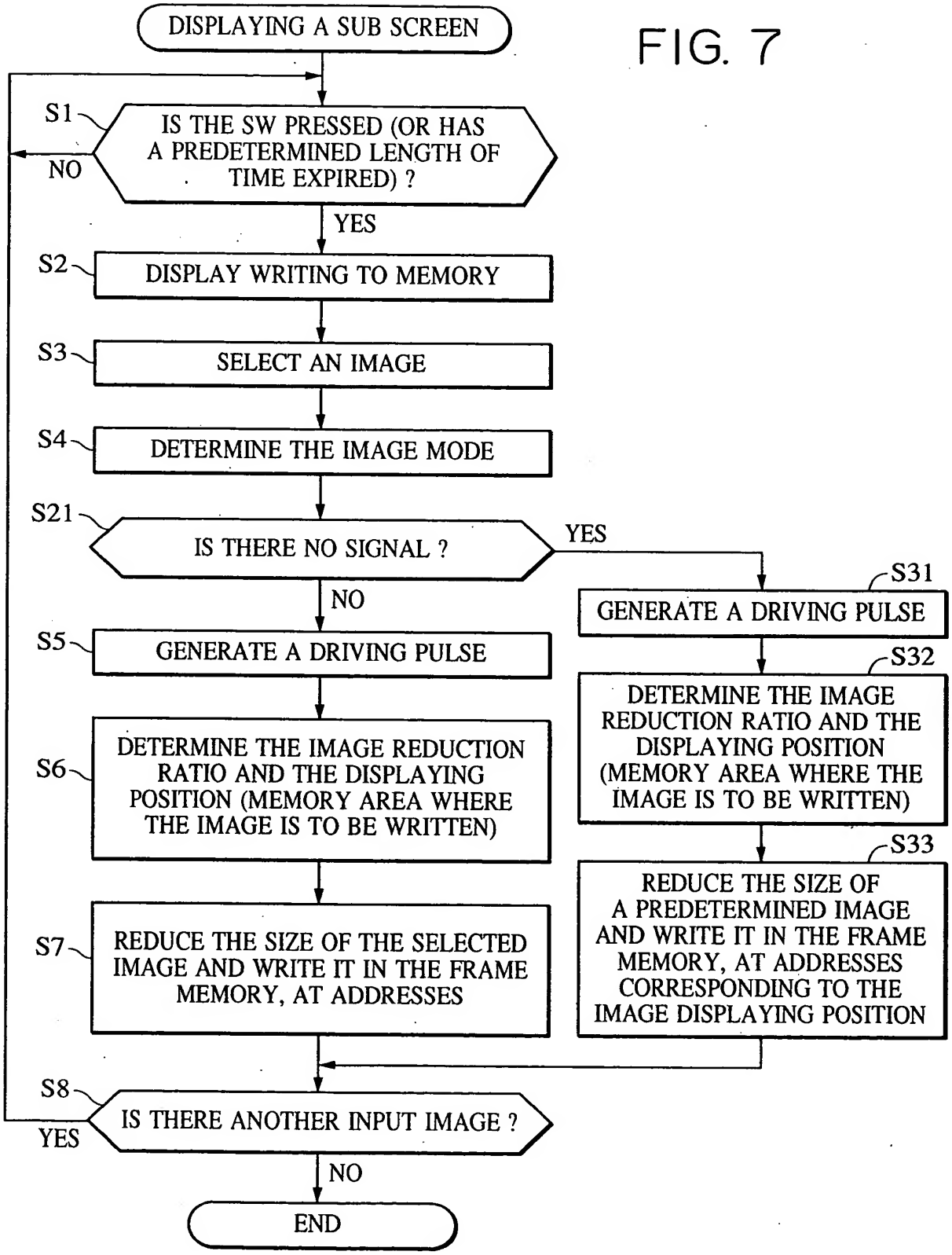

DISPLAYING A SUB SCREEN
S1
IS THE SW PRESSED (OR HAS A PREDETERMINED LENGTH OF TIME EXPIRED) ?
NO
YES
S2
DISPLAY WRITING TO MEMORY
S3
SELECT AN IMAGE
S4
DETERMINE THE IMAGE MODE
S21
IS THERE NO SIGNAL ?
YES
NO
S5
GENERATE A DRIVING PULSE
S6
DETERMINE THE IMAGE REDUCTION RATIO AND THE DISPLAYING POSITION (MEMORY AREA WHERE THE IMAGE IS TO BE WRITTEN)
S7
REDUCE THE SIZE OF THE SELECTED IMAGE AND WRITE IT IN THE FRAME MEMORY, AT ADDRESSES
S31
GENERATE A DRIVING PULSE
S32
DETERMINE THE IMAGE REDUCTION RATIO AND THE DISPLAYING POSITION (MEMORY AREA WHERE THE IMAGE IS TO BE WRITTEN)
S33
REDUCE THE SIZE OF A PREDETERMINED IMAGE AND WRITE IT IN THE FRAME MEMORY, AT ADDRESSES CORRESPONDING TO THE IMAGE DISPLAYING POSITION
S8
IS THERE ANOTHER INPUT IMAGE ?
YES
NO
END

FIG. 8

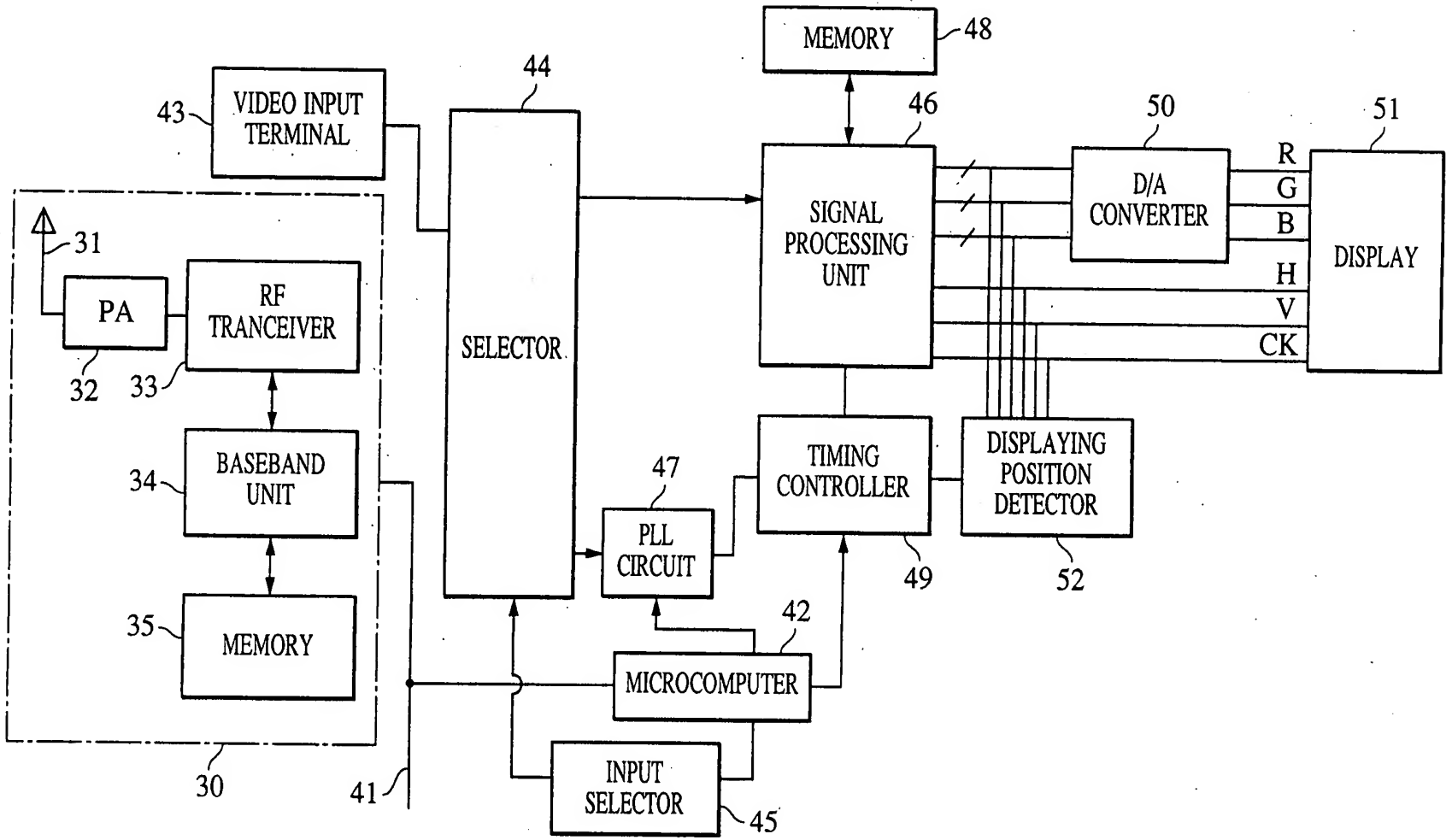

MEMORY
48
44
43
VIDEO INPUT TERMINAL
46
50
51
SIGNAL PROCESSING UNIT
D/A CONVERTER
R
G
B
H
V
CK
DISPLAY
31
PA
RF TRANCEIVER
SELECTOR
32
33
BASEBAND UNIT
34
47
PLL CIRCUIT
TIMING CONTROLLER
DISPLAYING POSITION DETECTOR
49
52
35
MEMORY
42
MICROCOMPUTER
INPUT SELECTOR
45
30
41

FIG. 9

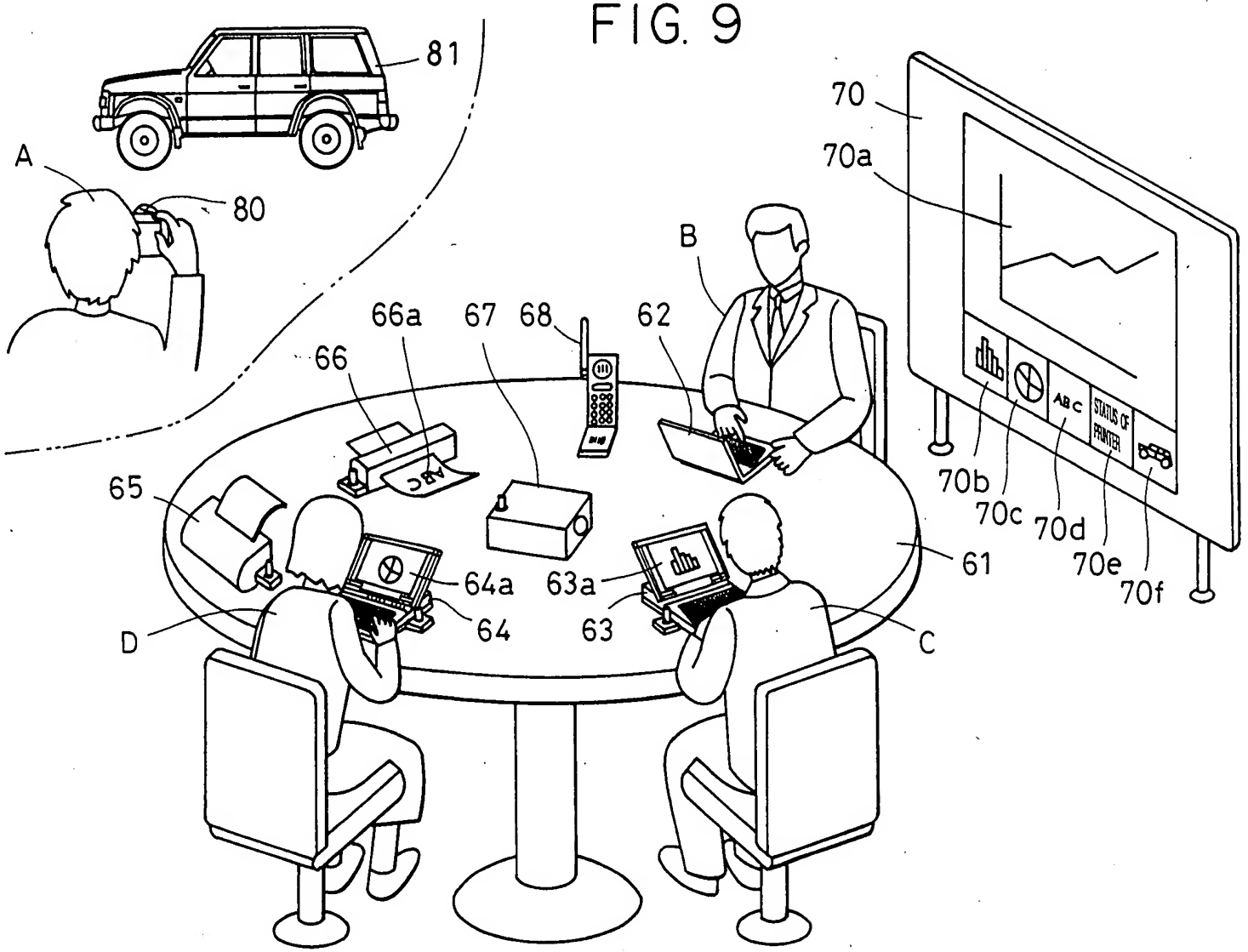

81
A
80
70
70a
B
66a
67
68
62
66
65
61
64a
63a
64
63
D
C
70b
70c
70d
70e
70f
ABC
STATUS OF PRINTER

FIG. 10A

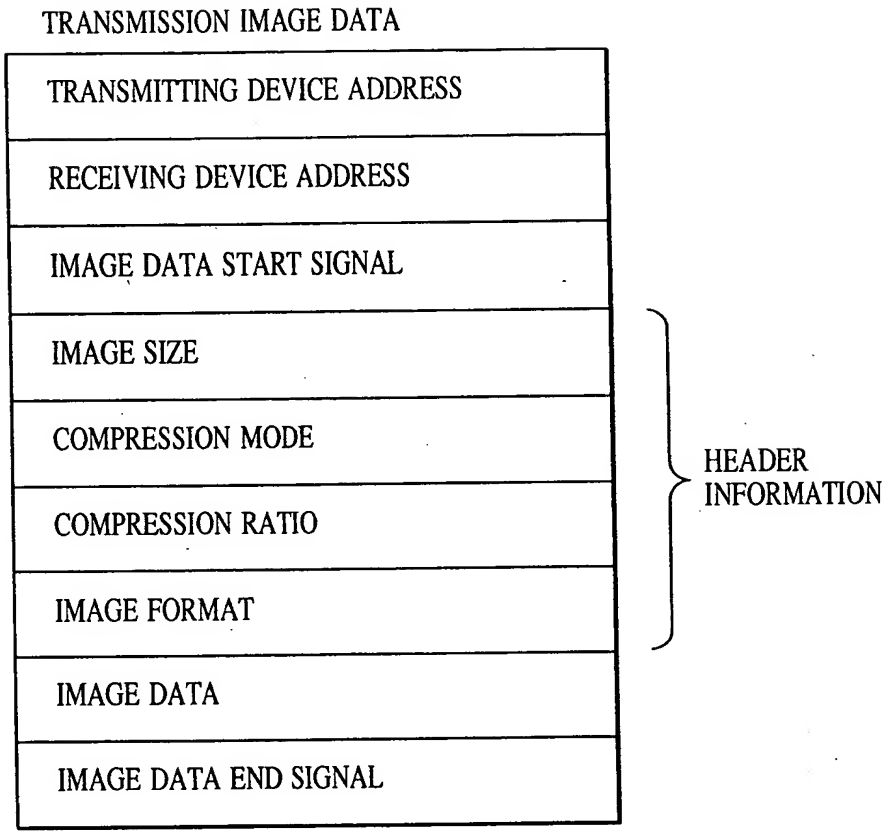


FIG. 10B

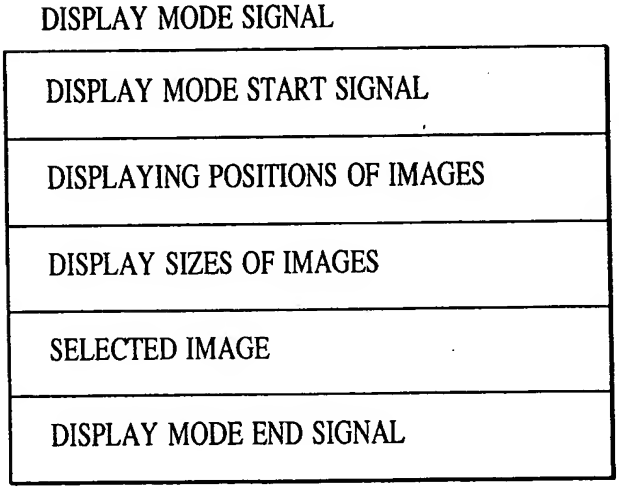


FIG. 11A

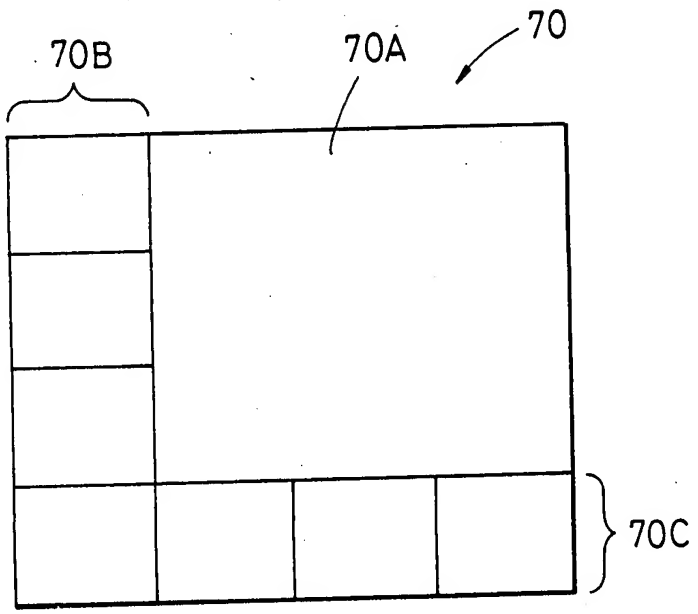


FIG. 11B

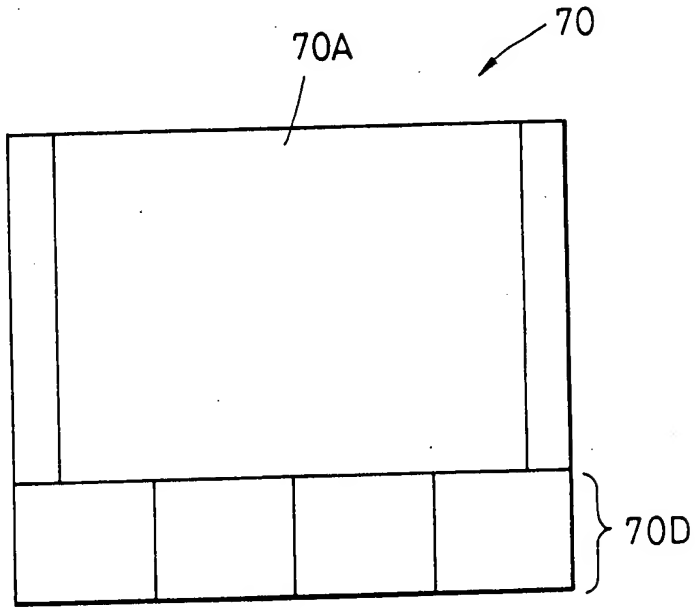


FIG. 12

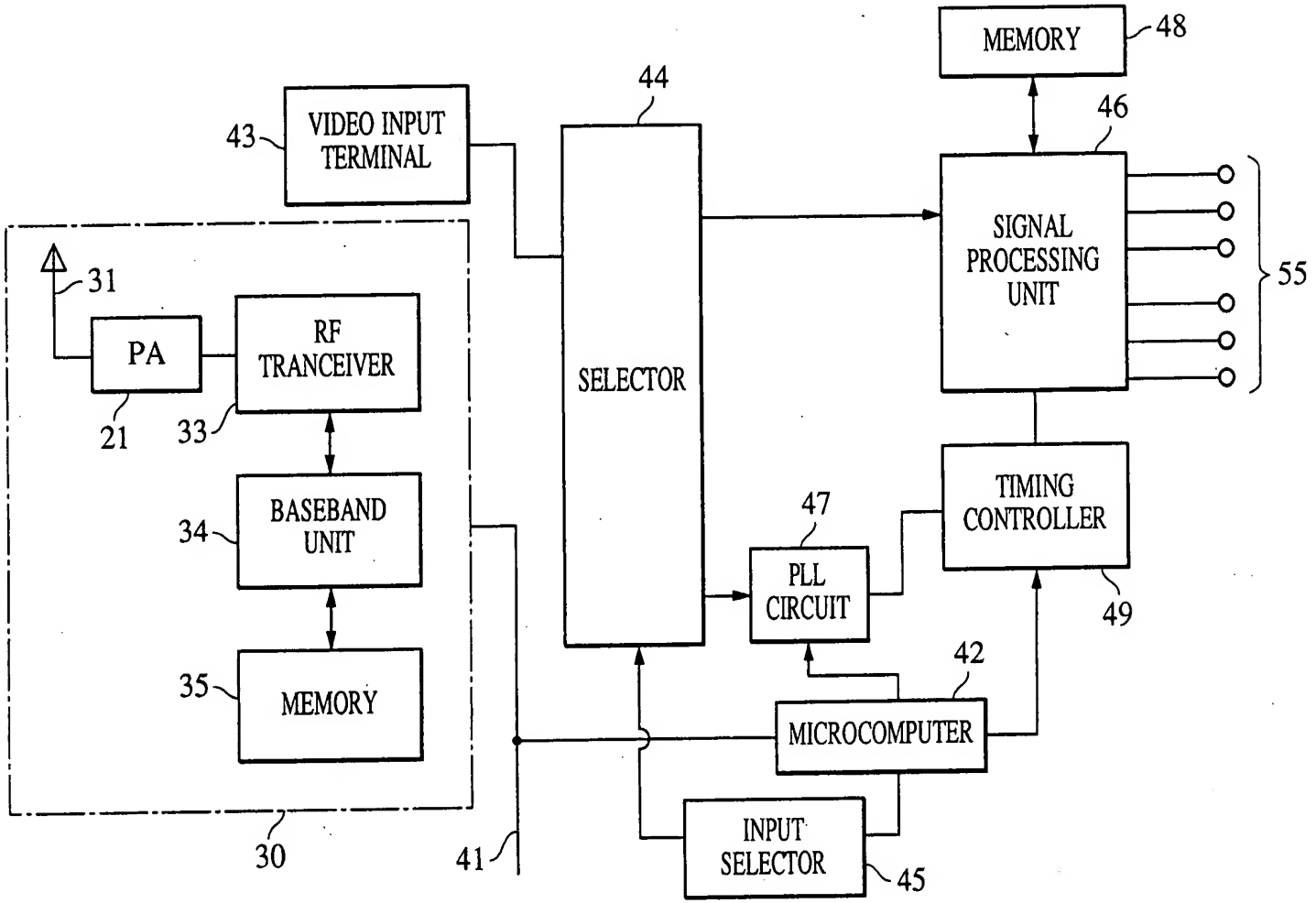

MEMORY
48
44
43
VIDEO INPUT TERMINAL
46
SIGNAL PROCESSING UNIT
55
SELECTOR
31
PA
RF TRANCEIVER
21
33
BASEBAND UNIT
34
47
TIMING CONTROLLER
PLL CIRCUIT
49
42
35
MEMORY
MICROCOMPUTER
INPUT SELECTOR
45
30
41

FIG. 13

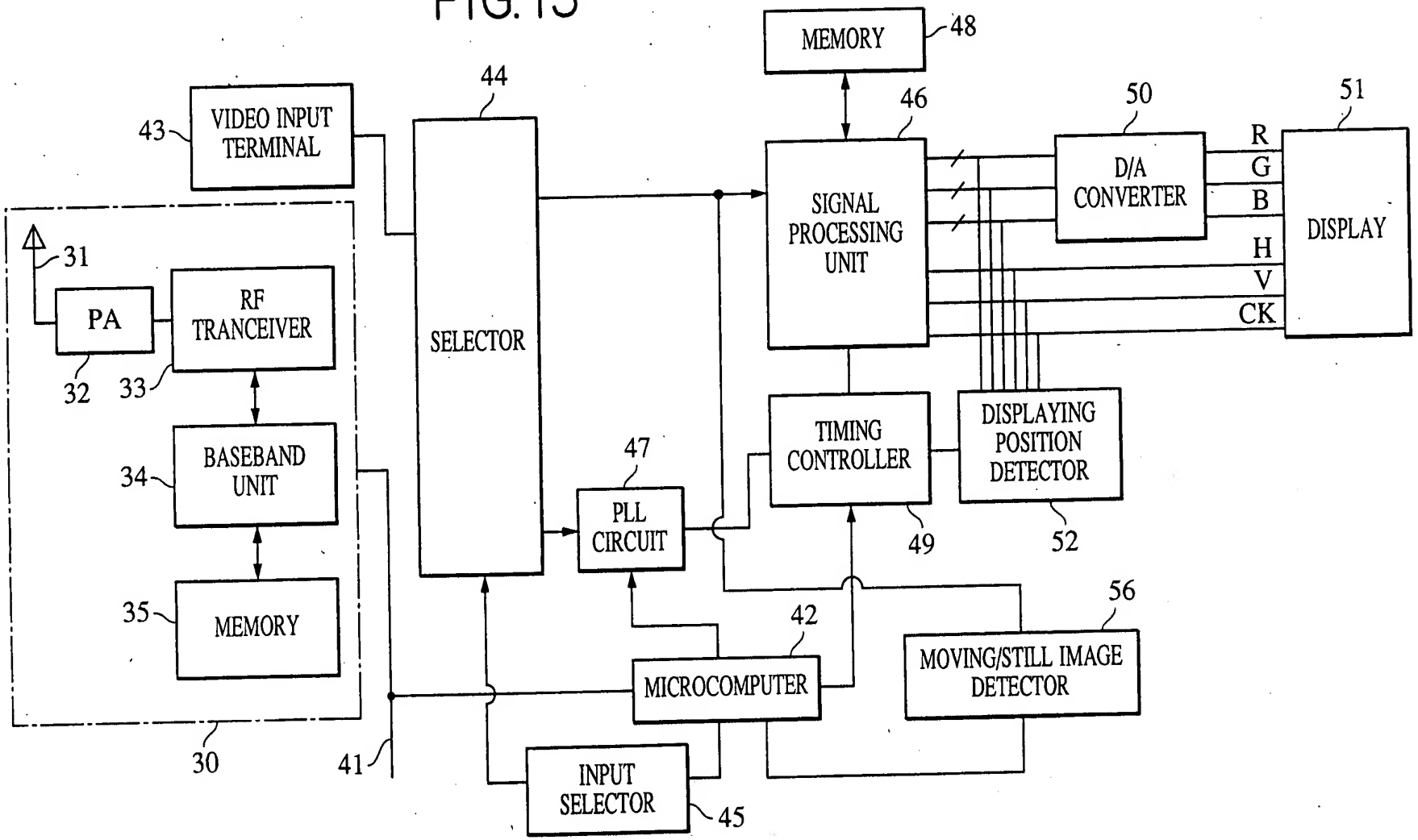
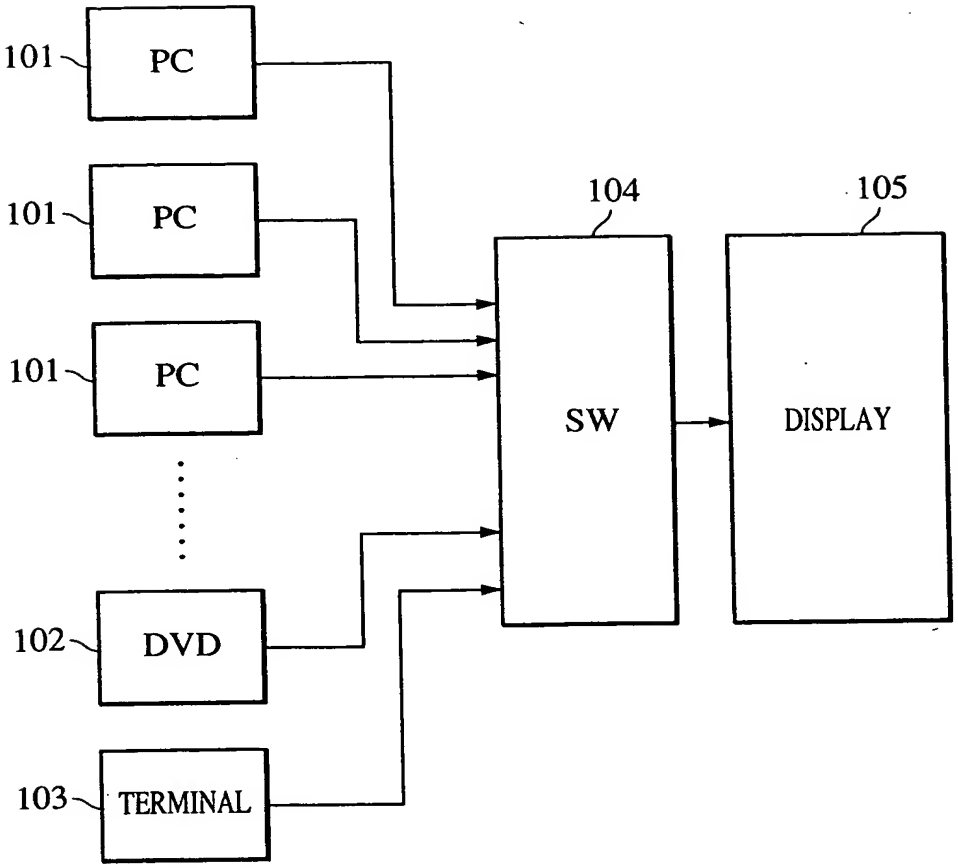

MEMORY
48
44
VIDEO INPUT TERMINAL
43
46
50
51
SELECTOR
SIGNAL PROCESSING UNIT
D/A CONVERTER
R
G
B
H
V
CK
DISPLAY
31
PA
RF TRANCEIVER
32
33
TIMING CONTROLLER
DISPLAYING POSITION DETECTOR
47
BASEBAND UNIT
34
PLL CIRCUIT
49
52
56
35
MEMORY
42
MOVING/STILL IMAGE DETECTOR
MICROCOMPUTER
30
41
INPUT SELECTOR
45

FIG. 14



(PRIOR ART)